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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): (William E. Corr) Examiner: Not yet assigned
Serial No.: (10/016,183)
Filed: (30 August 2001) Art Unit: 2133

Entitled: APPARATUS AND METHOD FOR DETERMINING EFFECT
OF ON-CHIP NOISE ON SIGNAL PROPAGATION

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Pursuant to the provisions of 37 C.F.R. Sections 1.56, 1.97(a)(3) and 1.98, applicant submits the nine (9) references listed on the attached PTO Form PTO/SB/08A to be considered by the Office for the above-captioned patent application filed on 30 August 2001.

The documents being submitted were cited by Examiner Rees of the United Kingdom Patent Office in a Search Report mailed on 22 August 2002 for a related U.K. Patent Application.

Because this supplemental information disclosure statement is being filed under 37 C.F.R. §1.97(a)(3) (i.e., before the mailing of a first Office action on the merits), no fee is due. The commissioner, however, is hereby authorized to charge any underpayment or credit any overpayment to our Deposit Account No. 20-0888.

Respectfully submitted,

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Dated: 30 September 2002

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